PATENT Attorney Docket No. ASC-044C1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S):

Eugene A. Fitzgerald et al.

SERIAL NO.:

Not Yet Assigned

GROUP NO.:

Not Yet Known

FILING DATE:

July 1, 2003

EXAMINER:

Not Yet Assigned

TITLE:

METHOD OF FABRICATING CMOS INVERTER AND

INTEGRATED CIRCUITS UTILIZING STRAINED SILICON

SURFACE CHANNEL MOSFETS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

Please enter the following amendment in advance of examination.

In the Specification

On page 1, please rewrite the paragraph on lines 5-6 to read as follows:

--This is a continuation of U.S. Serial No. 09/884,172, filed on June 19, 2001, and which claims priority from provisional application Serial No. 60/250,985, filed December 4, 2000.--

In the Claims

Please cancel claims 1-27 (i.e., all claims in the application) and add new claims 28-41.

Preliminary Amendment U.S. Serial No. *Not yet assigned* Page 2

Please charge any fee occasioned by this paper to our Deposit

Account No. 20-0531.

Respectfully submitted,

Date: July 1, 2003 Reg. No. 33,497

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